FIG. 1

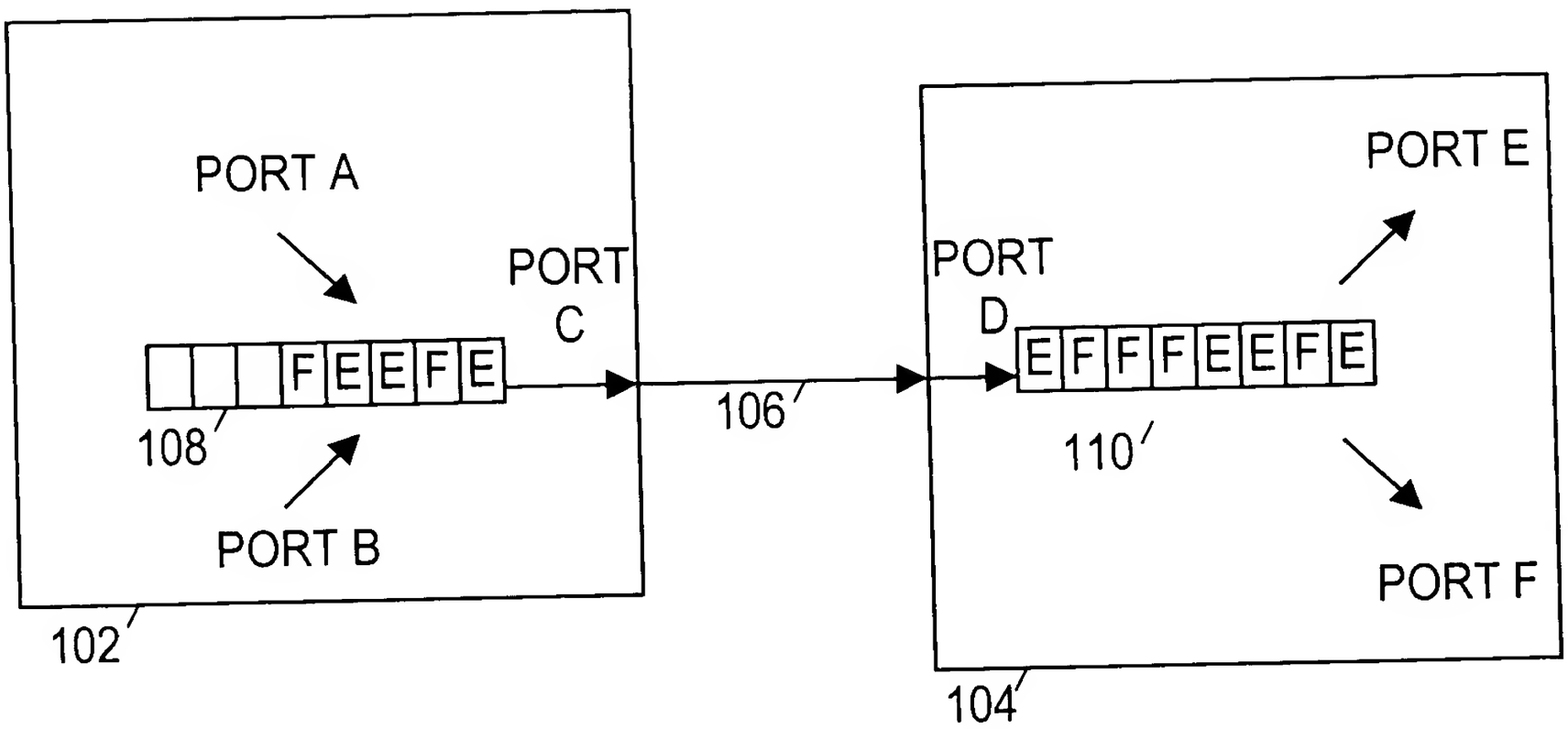
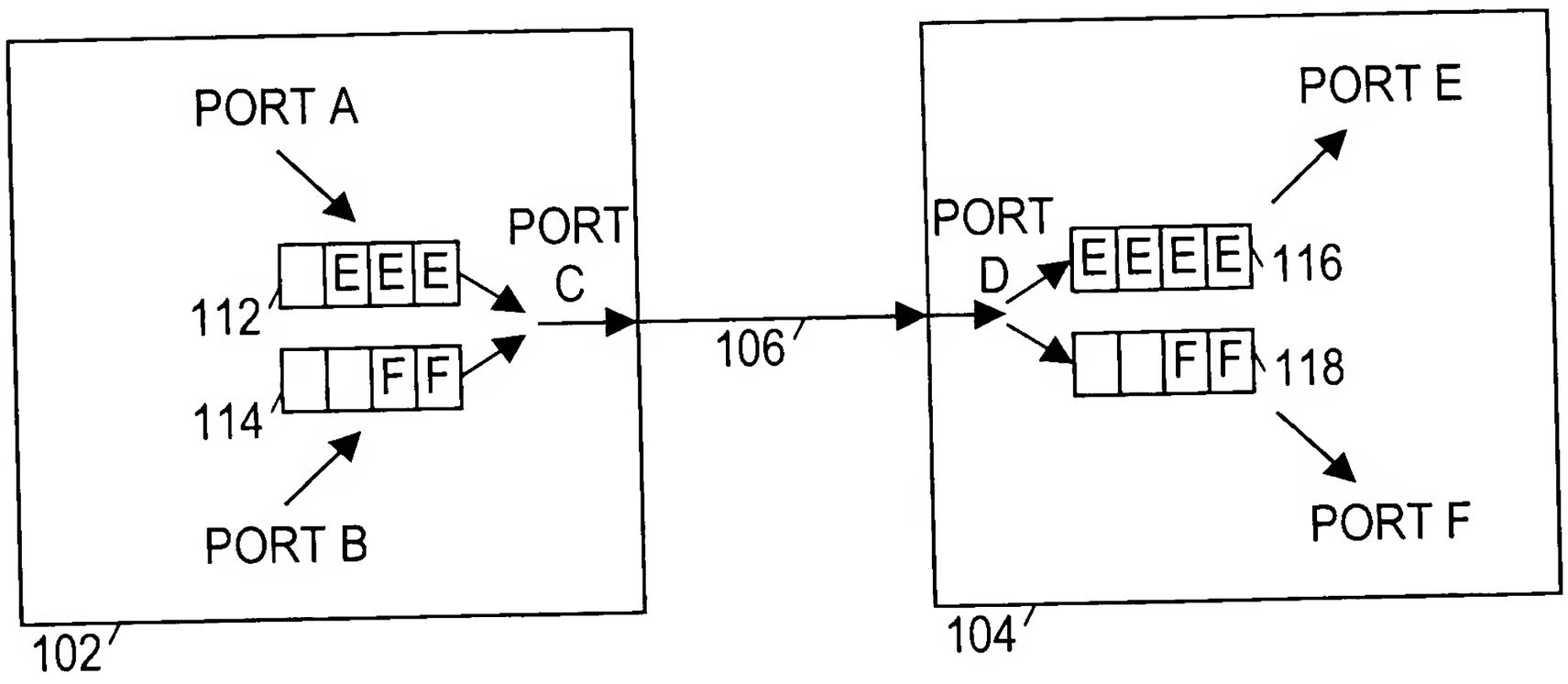

PORT A
PORT B
PORT C
PORT D
PORT E
PORT F
F E E F E
E F F F E E F E
106
108
110
102
104

FIG. 2


PORT A
PORT B
PORT C
PORT D
PORT E
PORT F
E E E
F F
E E E E
F F
112
114
116
118
106
102
104

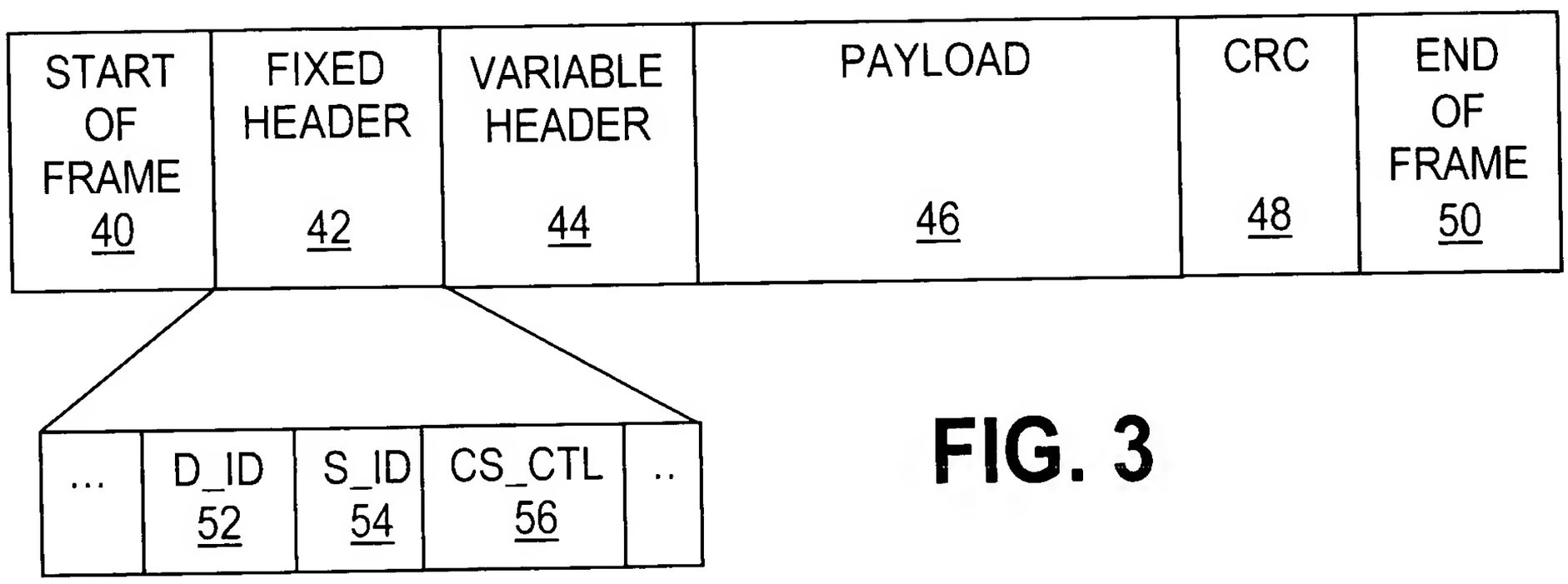

START OF FRAME 40
FIXED HEADER 42
VARIABLE HEADER 44
PAYLOAD 46
CRC 48
END OF FRAME 50
...
D_ID 52
S_ID 54
CS_CTL 56
..

FIG. 3

FIG. 4

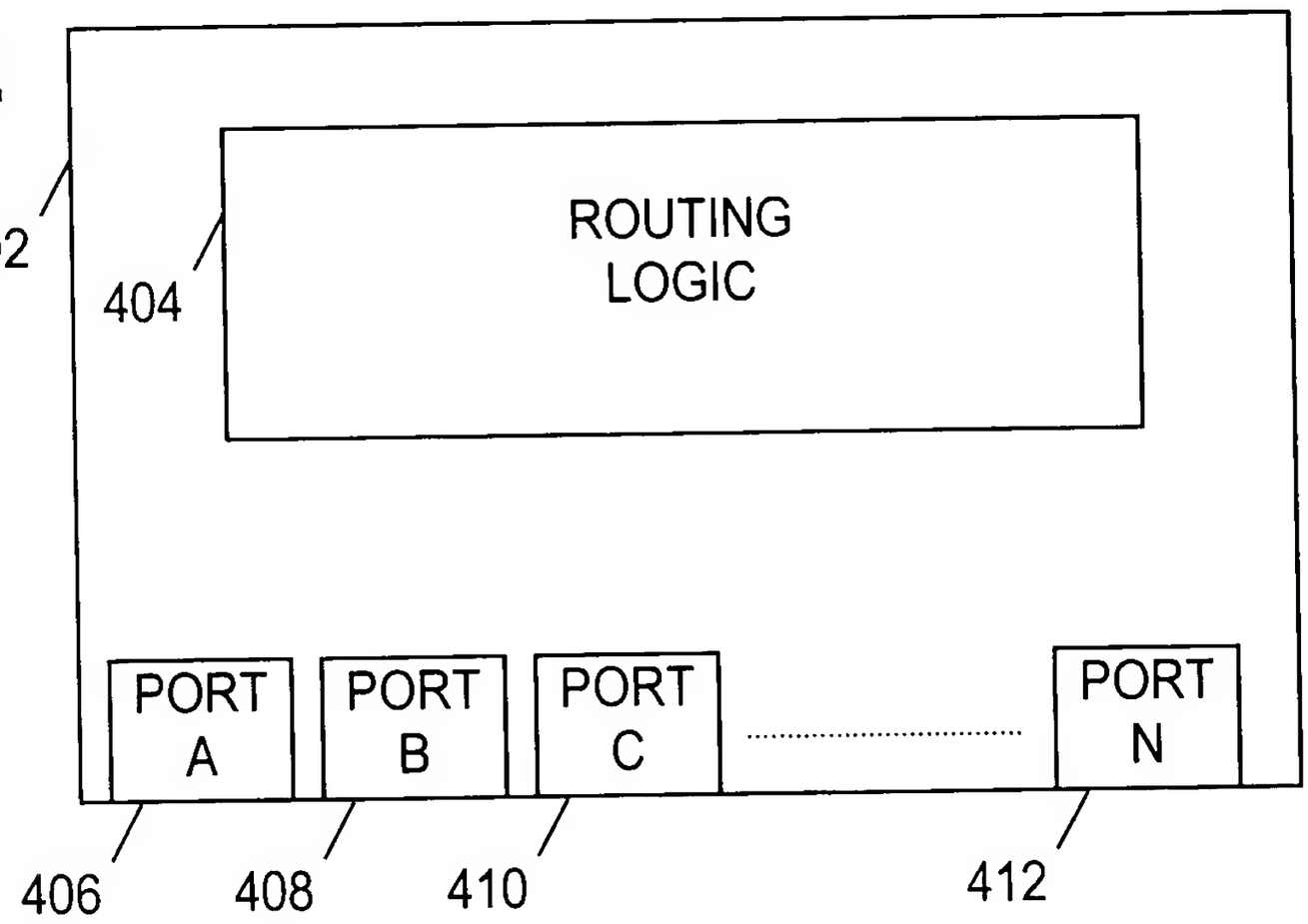

402
404
ROUTING LOGIC
PORT A
PORT B
PORT C
PORT N
406
408
410
412

FIG. 5

VC_REMAP1 BIT(S)	FUNCTION
63:60	**REMAP_VC_F:** The remapped VC when the calculated VC = 0xF.
59:56	**REMAP_VC_E:** The remapped VC when the calculated VC = 0xE.
55:52	**REMAP_VC_D:** The remapped VC when the calculated VC = 0xD.
51:48	**REMAP_VC_C:** The remapped VC when the calculated VC = 0xC.
47:44	**REMAP_VC_B:** The remapped VC when the calculated VC = 0xB.
43:40	**REMAP_VC_A:** The remapped VC when the calculated VC = 0xA.
39:36	**REMAP_VC_9:** The remapped VC when the calculated VC = 0x9.
35:32	**REMAP_VC_8:** The remapped VC when the calculated VC = 0x8.
31:28	**REMAP_VC_7:** The remapped VC when the calculated VC = 0x7.
27:24	**REMAP_VC_6:** The remapped VC when the calculated VC = 0x6.
23:20	**REMAP_VC_5:** The remapped VC when the calculated VC = 0x5.
19:16	**REMAP_VC_4:** The remapped VC when the calculated VC = 0x4.
15:12	**REMAP_VC_3:** The remapped VC when the calculated VC = 0x3.
11:8	**REMAP_VC_2:** The remapped VC when the calculated VC = 0x2.
7:4	**REMAP_VC_1:** The remapped VC when the calculated VC = 0x1.
3:0	**REMAP_VC_0:** The remapped VC when the calculated VC = 0x0.

FIG. 6

VC_REMAP1 BIT(S)	FUNCTION
63:60	**REMAP_VC_F:** The remapped VC when the calculated VC = 0xF.
59:56	**REMAP_VC_E:** The remapped VC when the calculated VC = 0xE.
55:52	**REMAP_VC_D:** The remapped VC when the calculated VC = 0xD.
51:48	**REMAP_VC_C:** The remapped VC when the calculated VC = 0xC.
47:44	**REMAP_VC_B:** The remapped VC when the calculated VC = 0xB.
43:40	**REMAP_VC_A:** The remapped VC when the calculated VC = 0xA.
39:36	**REMAP_VC_9:** The remapped VC when the calculated VC = 0x9.
35:32	**REMAP_VC_8:** The remapped VC when the calculated VC = 0x8.
31:28	**REMAP_VC_7:** The remapped VC when the calculated VC = 0x7.
27:24	**REMAP_VC_6:** The remapped VC when the calculated VC = 0x6.
23:20	**REMAP_VC_5:** The remapped VC when the calculated VC = 0x5.
19:16	**REMAP_VC_4:** The remapped VC when the calculated VC = 0x4.
15:12	**REMAP_VC_3:** The remapped VC when the calculated VC = 0x3.
11:8	**REMAP_VC_2:** The remapped VC when the calculated VC = 0x2.
7:4	**REMAP_VC_1:** The remapped VC when the calculated VC = 0x1.
3:0	**REMAP_VC_0:** The remapped VC when the calculated VC = 0x0.

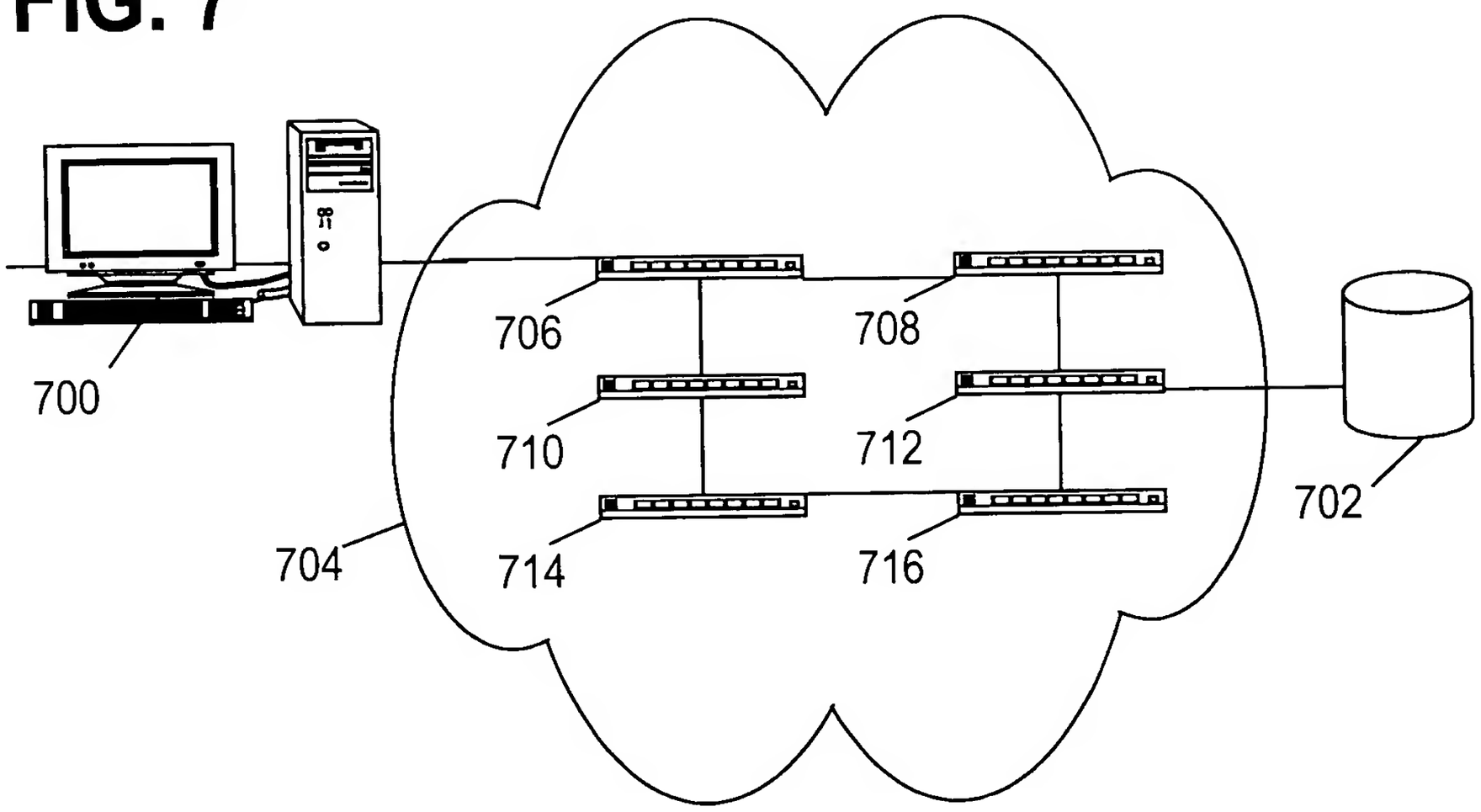



FIG. 7